

Rev	AWO #	Description	Date	Appr
		SEE SH1 1 OF 2 FOR REVISION		

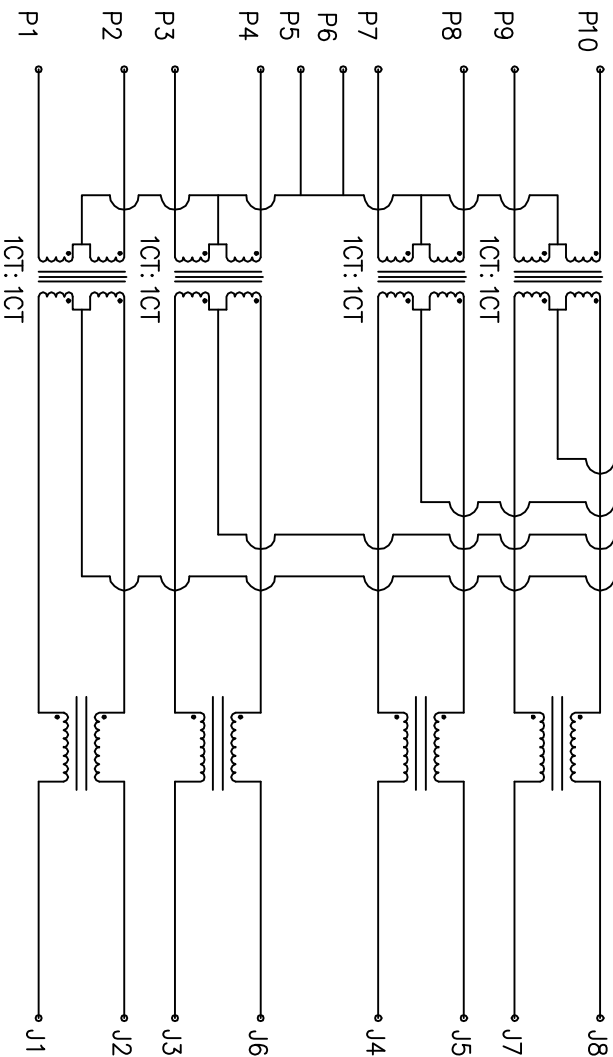
APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_

D



C



ELECTRICAL SPECIFICATIONS

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%  
(P3 ~ P4) : (J3 ~ J6) 1:1 ±3%  
(P7 ~ P8) : (J4 ~ J5) 1:1 ±3%  
(P9 ~ P10) : (J7 ~ J8) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
(P3 ~ P4) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
(P7 ~ P8) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
(P9 ~ P10) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.3uH Max. @ 1 MHz  
P3 ~ P4 (with J3 and J6 Short): 0.3uH Max. @ 1 MHz  
P7 ~ P8 (with J4 and J5 Short): 0.3uH Max. @ 1 MHz  
P9 ~ P10 (with J7 and J8 Short): 0.3uH Max. @ 1 MHz
- Interwinding Capacitor: (P1 ~ P2) : (J1 ~ J2): 35pF max. @ 1MHz  
(P3 ~ P4) : (J3 ~ J6): 35pF max. @ 1MHz  
(P7 ~ P8) : (J4 ~ J5): 35pF max. @ 1MHz  
(P9 ~ P10) : (J7 ~ J8): 35pF max. @ 1MHz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.  
(J4 ~ J5) : (J7 ~ J8): 1.2 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB min / 30MHZ to 80MHZ: -12dB min
- Dielectric Withstanding: (P1~P2) : (J1~J2) : (P3~P4) : (J3~J6) :1500V AC  
(P7~P8) : (J4~J5) : (P9~P10) : (J7~J8) :1500V AC
- Insertion Loss: RS=RL=100OHM : 100KHZ TO 80MHZ : -1.1dB Min.  
Output Voltage= 1V Peak :30ns Max.
- Rise Time: RS=100 Ohms and RL=100 ohms  
Pulse Width= 112ns :30ns Max.
- Cross Talk: 1MHZ to 100MHZ: 40dB Typ.  
10. Common to Common mode Attenuation 30MHZ to 100MHZ: 35dB Typ.

A

UNLESS OTHERWISE SPECIFIED  
ALL DIMENSIONS ARE INCHES [MM]  
TOLERANCES: EXCEPT AS NOTED

.X	± .10	Ø.X	± .10
.XX	± .020	Ø.XXX	± .015
.XXX	± .015	Ø.XXX	± .010

APPROVALS

DRAWN	DY	8/27/09
CHECKED		
APPROVED		

TITLE: MODULAR JACK GANGED, 5 PORT, 8P8C  
RIGHT ANGLE PCB

909 Rahway Avenue, Union, NJ 07083  
Phone: 908-687-5000 Fax: 908-687-5710

ADAM TECH

SIZE	PART NO.	REV.
X	MTJG-5-88TX1-FSB-M62	-

REF: S0008C SCALE: NTS SHEET 2 OF 2