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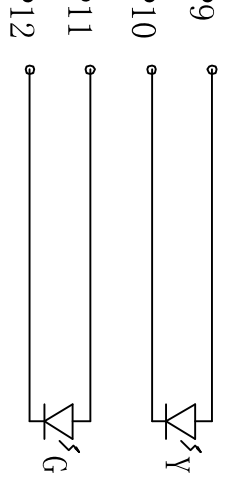
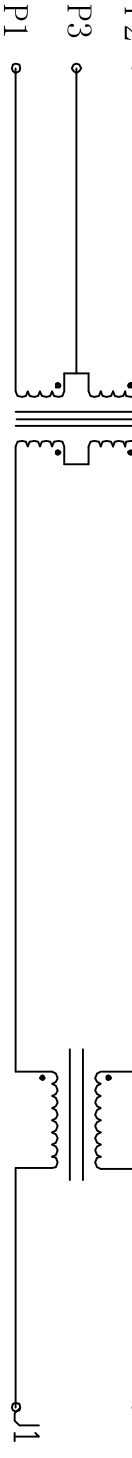
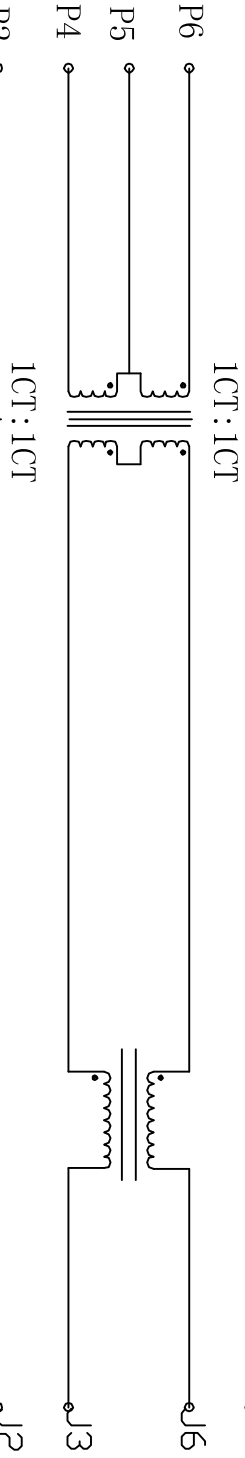
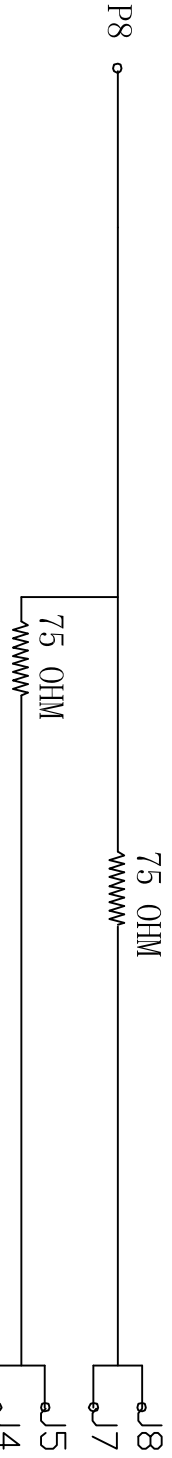
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Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



**ELECTRICAL SPECIFICATIONS:**

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%  
(P4 ~ P6) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
(P4 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.3uH Max. @ 1 MHz  
P4 ~ P6 (with J3 and J6 Short): 0.3uH Max. @ 1 MHz
- Interwinding Capacitor: (P1 ~ P2) : (J1 ~ J2): 30pF max. @ 1MHz  
(P4 ~ P6) : (J3 ~ J6): 30pF max. @ 1mhz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB min / 30MHZ to 80MHZ: -12dB min
- Dielectric Withstanding: (P1~P2) : (J1~J2): (P4~P6) : (J3~J6) : 1500V AC
- Insertion Loss: RS=RL=100OHM : 100KHZ TO 80MHZ : -1.2dB Min.
- Cross Talk: 1MHZ to 100MHZ: 35dB Typ.
- Common to Common mode Attention 30MHZ to 100MHZ: 30dB Typ.

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE INCHES [MM]	
TOLERANCES: EXCEPT AS NOTED	
INCHES	HOLES
.X ± .10	Ø.X ± .10
.XX ± .020	Ø.XX ± .015
.XXX ± .015	Ø.XXX ± .010
APPROVALS	
DRAWN	DY
CHECKED	
APPROVED	
	DATE
	10/29/09

**ADAM TECH**  
 909 Rahway Avenue, Union, NJ 07083  
 Phone: 908-687-5000 Fax: 908-687-5710

TITLE: MODULAR JACK GANGED, 1 PORT, 8P8C  
 W/ LED1 YELLOW, LED2 GREEN

SIZE	PART NO.	REV.
X	MTJ-88TX1-FSP-PG-LG-M11	
REF:	S00006C	
SCALE:	NTS	
SHEET	2 OF 2	

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