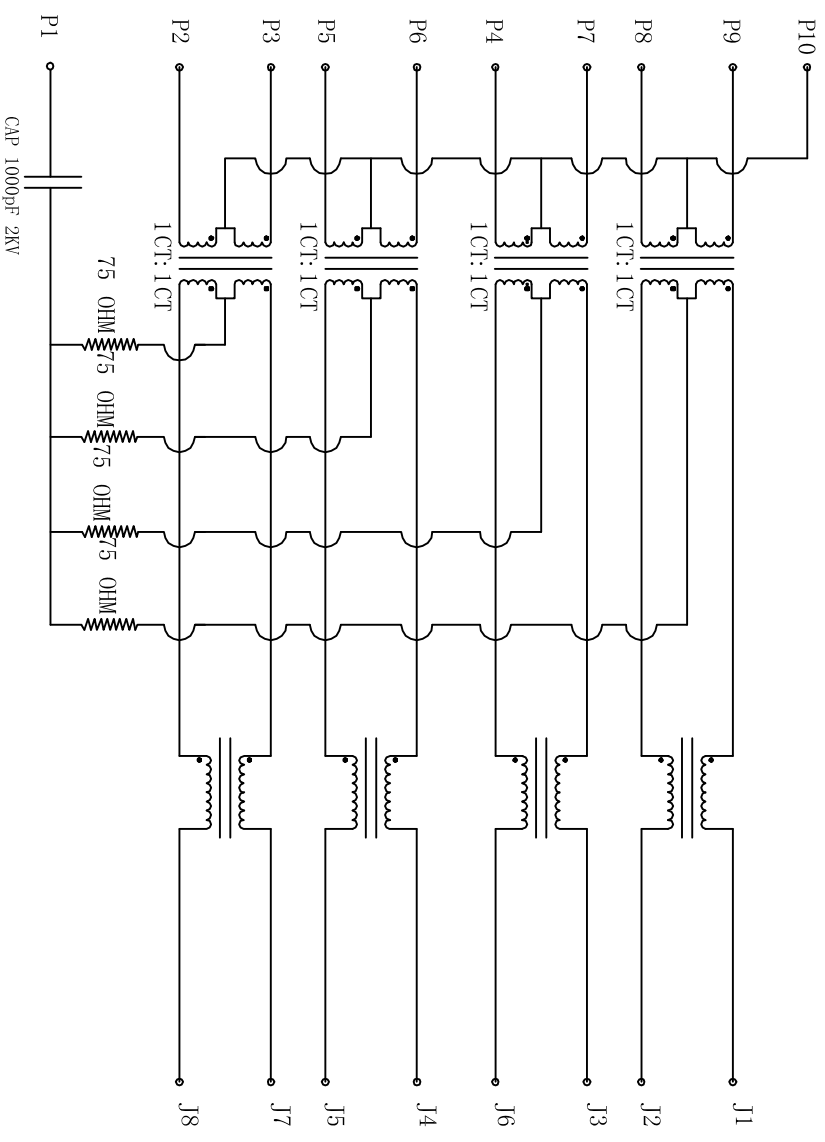


Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: _____

DATE: _____ TITLE: _____



- ELECTRICAL SPECIFICATIONS**
- Turns Ratio: (P9 ~ P8) : (J1 ~ J2) : 1:1 ±3%
 (P7 ~ P4) : (J3 ~ J6) : 1:1 ±3%
 (P6 ~ P5) : (J4 ~ J5) : 1:1 ±3%
 (P3 ~ P2) : (J7 ~ J8) : 1:1 ±3%
 - Inductance: (P9 ~ P8) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P7 ~ P4) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P6 ~ P5) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P3 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 - Leakage Inductance: P9 ~ P8 (with J1 and J2 Short): 0.35uH Max. @ 1 MHZ
 P7 ~ P4 (with J3 and J6 Short): 0.35uH Max. @ 1 MHZ
 P6 ~ P5 (with J4 and J5 Short): 0.35uH Max. @ 1 MHZ
 P3 ~ P2 (with J7 and J8 Short): 0.35uH Max. @ 1 MHZ
 - Interwinding Capacitor: (P9 ~ P8) : (J1 ~ J2): 115pF max. @ 1MHZ
 (P7 ~ P4) : (J3 ~ J6): 115pF max @ 1MHZ
 (P6 ~ P5) : (J4 ~ J5): 115pF max @ 1MHZ
 (P3 ~ P2) : (J7 ~ J8): 115pF max @ 1MHZ
 - DC Resistance: (J1 ~ J2) : (J3 ~ J6) : (J7 ~ J8): 1.2 Ohms Max.
 - Dielectric Withstanding: (P9~P8) : (J1~J2): (P7~P4) : (J3~J6) : 1500V AC OR 2250VDC
 (P6~P5) : (J4~J5): (P3~P2) : (J7~J8) : 1500V AC OR 2250VDC
 - Insertion Loss: 0.5MHZ TO 100MHZ: -1.2dB MAX
 (P6~P5) : (J4~J5) : (P3~P2) : (J7~J8) : -1.2dB MAX
 - Return Loss: 0.5MHZ TO 40MHZ: -18dB MIN
 40.1MHZ TO 100MHZ: -12-20LOG(f/80)dB MIN (WHERE f IS IN MHZ)
 - Cross Talk: 0.5MHZ TO 40MHZ: -35dB MIN
 40.1MHZ TO 100MHZ: -33-20LOG(f/50)dB MIN (WHERE f IS IN MHZ).
 - Common to Common mode Attenuation: 0.5MHZ TO 100MHZ: -30dB MIN

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MODULAR JACK GANGED, 1 PORT, 8P8C

UNLESS OTHERWISE SPECIFIED
 ALL DIMENSIONS ARE INCHES [MM]
 TOLERANCES: EXCEPT AS NOTED

INCHES	HOLES
.X ± .10	Ø.X ± .10
.XX ± .020	Ø.XXX ± .015
.XXX ± .015	Ø.XXX ± .010

APPROVALS

DATE	DATE
	6/10/10

DRAWN: DY
 CHECKED: _____
 APPROVED: _____

SIZE	PART NO.	REV.
X	MTJ-88TUX1-FSD-PG-M104	A
REF:	S00005C	
SCALE:	NTS	
SHEET	2 OF 2	