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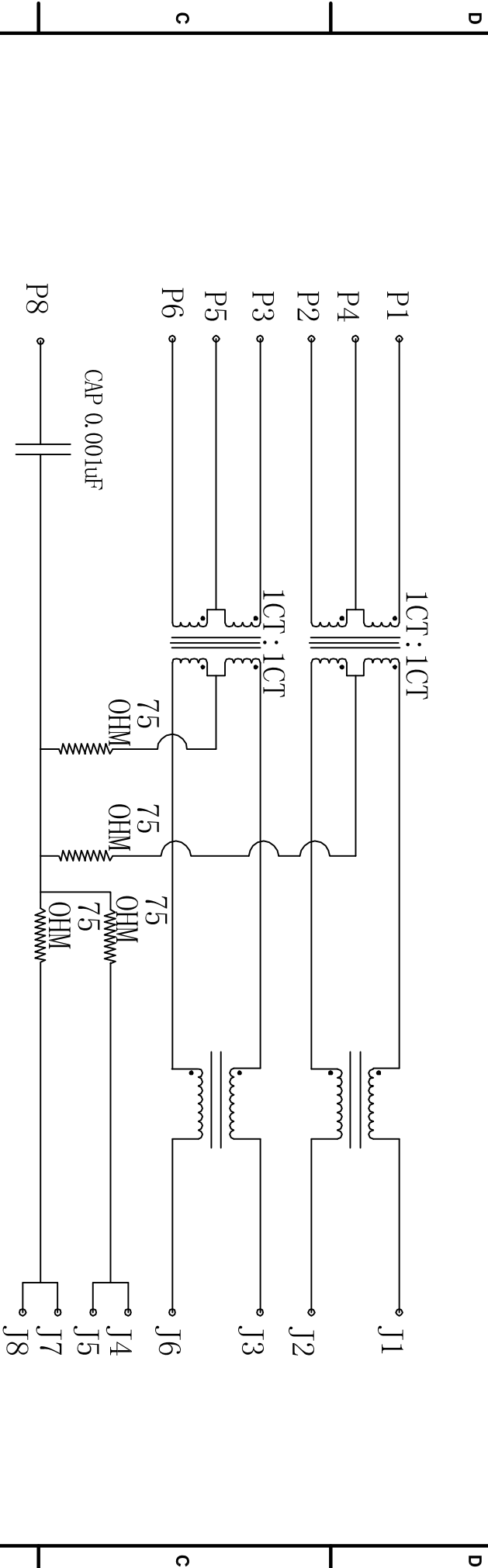
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Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: _____

DATE: _____ TITLE: _____



ELECTRICAL SPECIFICATIONS:

- Turns Ratio: (P1 ~ P2) : (J1 ~ J2) 1:1 ±3%
(P3 ~ P6) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P1 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
(P3 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P2 (with J1 and J2 Short): 0.35uH Max. @ 1 MHz
P3 ~ P6 (with J3 and J6 Short): 0.35uH Max. @ 1 MHz
- Interwinding Capacitance: (P1 ~ P2) : (J1 ~ J2): 35pF max. @ 1MHz
(P3 ~ P6) : (J3 ~ J6): 35pF max @ 1MHz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.
- Return Loss: 30MHz: -18dB MIN
30MHz to 60MHz: -(18-20log(f/30))dB MIN (WHERE f IS IN MHZ)
60MHz to 80MHz: -12dB MIN
- Dielectric Withstanding: (P1~P2) : (J1~J2): (P3~P6) : (J3~J6) :2250VDC
- Insertion Loss: 100KHZ TO 100MHZ : -1.2dB Min.
- Cross Talk: 1MHZ to 100MHZ: -35dB Typ.
- Common to Common mode Attenuation: 30MHZ TO 100MHZ: -30dB Typ.

ADAM TECH

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MODULAR JACK GANGED, 1 PORT, 8P8C
W/ LED1 YELLOW, LED2 GREEN

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE INCHES [MM] TOLERANCES: EXCEPT AS NOTED		APPROVALS		DATE	
.X	± .10	Ø.X	± .10		
.XX	± .020	Ø.XXX	± .015		
.XXX	± .015	Ø.XXX	± .010		
DRAWN	DY		10/07/10		
CHECKED					
APPROVED					
REF: S00006C	SCALE: NTS	SHEET 2 OF 2			

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