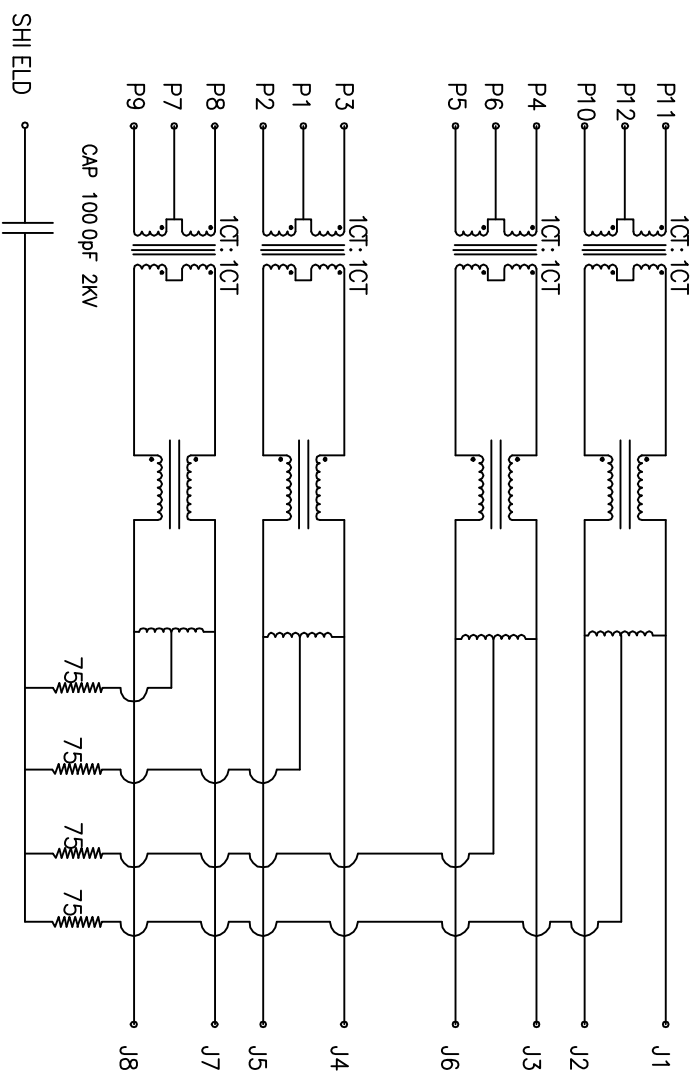


Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



**ELECTRICAL SPECIFICATIONS**

- Turns Ratio: (P11 ~ P10) : (J1 ~ J2) 1:1 ±3%  
 (P4 ~ P5) : (J3 ~ J6) 1:1 ±3%  
 (P3 ~ P2) : (J4 ~ J5) 1:1 ±3%  
 (P8 ~ P9) : (J7 ~ J8) 1:1 ±3%
- Inductance: (P11 ~ P10) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P4 ~ P5) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P3 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P8 ~ P9) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P11 ~ P10 (with J1 and J2 Short): 0.35uH Max. @ 1 MHz  
 P4 ~ P5 (with J3 and J6 Short): 0.35uH Max. @ 1 MHz  
 P3 ~ P2 (with J4 and J5 Short): 0.35uH Max. @ 1 MHz  
 P8 ~ P9 (with J7 and J8 Short): 0.35uH Max. @ 1 MHz
- Interwinding Capacitor: (P11 ~ P10) : (J1 ~ J2): 105pF max. @ 1MHz  
 (P4 ~ P5) : (J3 ~ J6): 105pF max @ 1MHz  
 (P3 ~ P2) : (J4 ~ J5): 105pF max @ 1MHz  
 (P8 ~ P9) : (J7 ~ J8): 105pF max @ 1MHz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6) : (J4 ~ J5) : (J7 ~ J8): 1.2 Ohms Max.
- Return Loss: 1MHz to 30MHz: -19dB min /30MHz to 60MHz: -13dB min  
 60MHz to 80MHz: -12dB min /80MHz to 100MHz: -10dB min
- Dielectric Withstanding: (P11~P10) : (J1~J2) : (P4~P5) : (J3~J6) : 1500V AC  
 (P3~P2) : (J4~J5) : (P8~P9) : (J7~J8) : 1500V AC
- Insertion Loss: RS=RL=1000OHM 100KHZ TO 100MHZ:-1.2dB Min
- Cross Talk: 1MHZ to 100MHZ: -[33-20LOG(F/50MHZ)]Min
- Common to Common mode Attenuation 30MHz to 100MHz: 30dB Typ.

UNLESS OTHERWISE SPECIFIED  
 ALL DIMENSIONS ARE INCHES [MM]  
 TOLERANCES: EXCEPT AS NOTED  
 INCHES                    HOLES  
 .X                    ± .10                    Ø.X  
 .XX                    ± .020                    Ø.XXX  
 .XXX                    ± .015                    Ø.XXX  
 .XXX                    ± .010

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APPROVALS	DATE	TITLE
DRAWN	DY	9/25/09
CHECKED		
APPROVED		
SIZE	PART NO.	REV.
X	MTJ-88CX1-FSP-PG-M20	-
REF:	S0008C	SCALE: NTS
		SHEET 2 OF 2