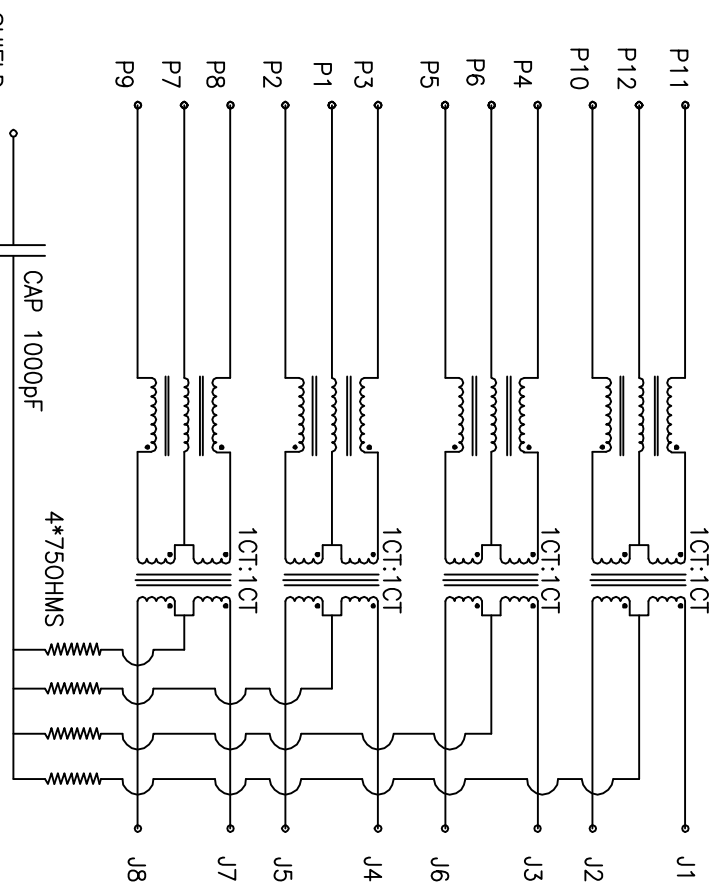


Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: \_\_\_\_\_

DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



**ELECTRICAL SPECIFICATIONS**

- Turns Ratio: (P11 ~ P10) : (J1 ~ J2) : 1:1 ±3%  
 (P4 ~ P5) : (J3 ~ J6) : 1:1 ±3%  
 (P3 ~ P2) : (J4 ~ J5) : 1:1 ±3%  
 (P8 ~ P9) : (J7 ~ J8) : 1:1 ±3%
- Inductance: (P11 ~ P10) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P4 ~ P5) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P3 ~ P2) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias  
 (P8 ~ P9) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P11 ~ P10 (with J1 and J2 Short): 0.3uH Max. @ 1 MHz  
 P4 ~ P5 (with J3 and J6 Short): 0.3uH Max. @ 1 MHz  
 P3 ~ P2 (with J4 and J5 Short): 0.3uH Max. @ 1 MHz  
 P8 ~ P9 (with J7 and J8 Short): 0.3uH Max. @ 1 MHz
- Interwinding Capacitance: (P11 ~ P10) : (J1 ~ J2): 35pf max. @ 1MHz  
 (P4 ~ P5) : (J3 ~ J6): 35pf max @ 1MHz  
 (P3 ~ P2) : (J4 ~ J5): 35pf max @ 1MHz  
 (P8 ~ P9) : (J7 ~ J8): 35pf max @ 1MHz
- DC Resistance: (J1 ~ J2) : (J3 ~ J6) : (J7 ~ J8): 1.2 Ohms Max.
- Return Loss: 0.5MHz to 40MHz: 18dB min / 40.1MHz to 100MHz: 12-20LOG(f/80)dB min
- Dielectric Withstanding: (P11~P10) : (J1~J2) : (P4~P5) : (J3~J6) : 2250VDC  
 (P3~P2) : (J4~J5) : (P8~P9) : (J7~J8) : 2250VDC
- Insertion Loss: 0.5MHz TO 100MHz: 1.1dB Min  
 RS=1000HMS and RL=1000HMS  
 Output voltage= 1V peak: 3.0ns Max
- Rise Time: 0.5MHz to 40MHz: 35dB Min  
 Pulse width= 112ns: 3.0ns Max
- Cross Talk: 0.5MHz to 40MHz: 35dB Min  
 40.1MHz to 100MHz: 33-20LOG(f/50)dB Min
- Common to Common mode Attenuation 0.5MHz to 100MHz: 30dB Min.

UNLESS OTHERWISE SPECIFIED  
 ALL DIMENSIONS ARE INCHES [MM]  
 TOLERANCES: EXCEPT AS NOTED

.X	± .10	Ø.X	HOLES	± .10
.XX	± .020	Ø.XXX		± .015
.XXX	± .015	Ø.XXX		± .010

APPROVALS	DATE
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CHECKED	
APPROVED	

**ADAM TECH**  
 909 Rahway Avenue, Union, NJ 07083  
 Phone: 908-687-5000 Fax: 908-687-5710

TITLE	MODULAR JACK GANGED, 1 PORT, 8P8C
	W/LED1 YELLOW, LED2 GREEN
SIZE	PART NO.
X	MTJ-88CX1-FSP-PG-LG-M20
REF: S0008C	SCALE: NTS
	SHEET 2 OF 2