

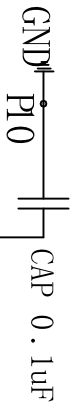
Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

DATE: _____

TITLE: _____

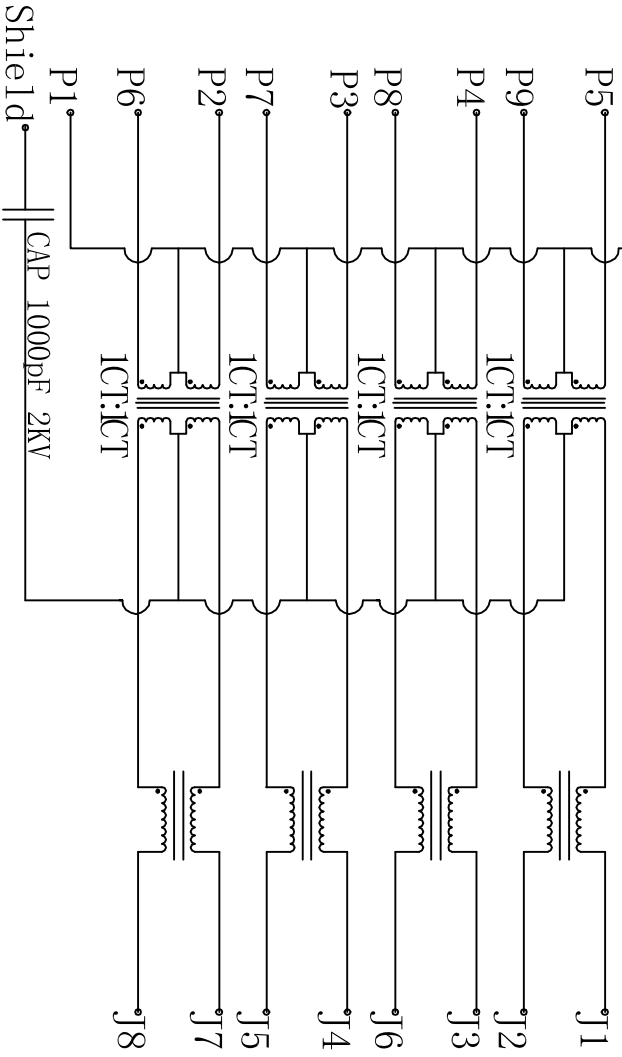
APPROVED: _____

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- ELECTRICAL SPECIFICATIONS
- Turns Ratio: (P5 ~ P9) : (J1 ~ J2) 1:1 ±3%
 (P4 ~ P8) : (J3 ~ J6) 1:1 ±3%
 (P3 ~ P7) : (J4 ~ J5) 1:1 ±3%
 (P2 ~ P6) : (J7 ~ J8) 1:1 ±3%

- Inductance: (P5 ~ P9) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P4 ~ P8) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P3 ~ P7) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
 (P2 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P5 ~ P9 (with J1 and J2 Short): 0.3uH Max. @ 1 MHz
 P4 ~ P8 (with J3 and J6 Short): 0.3uH Max. @ 1 MHz
 P3 ~ P7 (with J4 and J5 Short): 0.3uH Max. @ 1 MHz
 P2 ~ P6 (with J7 and J8 Short): 0.3uH Max. @ 1 MHz
- Interwinding Capacitor: (P5 ~ P9) : (J1 ~ J2): 105pF max. @ 1MHZ
 (P4 ~ P8) : (J3 ~ J6): 105pF max @ 1MHZ
 (P3 ~ P7) : (J4 ~ J5): 105pF max @ 1MHZ
 (P2 ~ P6) : (J7 ~ J8): 105pF max @ 1MHZ
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.
 (J4 ~ J5) : (J7 ~ J8): 1.2 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB min / 30MHZ to 80MHZ: -12dB min
- Dielectric Withstanding: (P5~P9) : (J1~J2) : (P4~P8) : (J3~J6) :1500V AC
 (P3~P7) : (J4~J5) : (P2~P6) : (J7~J8) :1500V AC
- Insertion Loss: RS=RL=1000OHM : 100KHZ TO 80MHZ : -1.1dB Min.
 Output Voltage= 1V Peak :3.0NS Max.
- Rise Time: RS=100 Ohms and RL=100 ohms
- Cross Talk: 1MHZ to 100MHZ: 40dB Typ.
- Common to Common mode Attenuation 30MHZ to 100MHZ: 35dB Typ.



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MODULAR JACK GANGED, 1 PORT, 8P8C

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE INCHES [MM]	
TOLERANCES: EXCEPT AS NOTED	
INCHES	HOLES
.X ±.10	Ø.X ±.10
.XX ±.020	Ø.XXX ±.015
.XXX ±.015	Ø.XXX ±.010
APPROVALS	
DRAWN	DY
CHECKED	
APPROVED	
	DATE 9/11/09

SIZE	PART NO.	REV.
X	MTJ-88CPX-FSJ-M75	-
REF:	S0008C	
SCALE:	NTS	
SHEET	2 OF 2	