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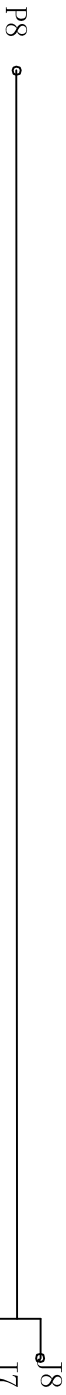
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Rev	AWO #	Description	Date	Appr
		SEE SHT 1 OF 2 FOR REVISION		

APPROVED: _____

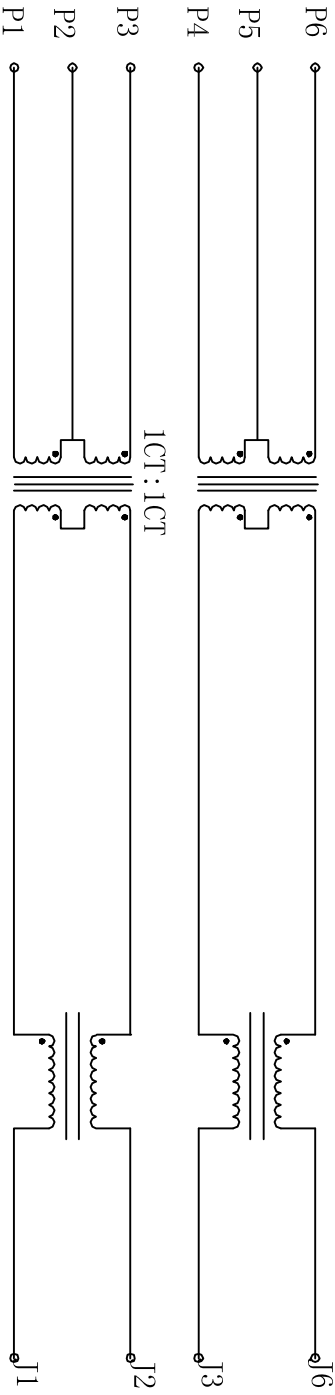
DATE: _____ TITLE: _____

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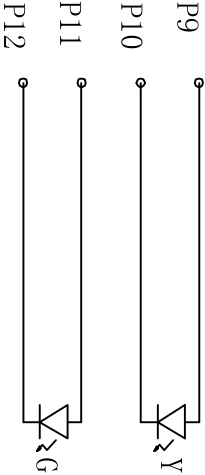
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C



C

B



B

ELECTRICAL SPECIFICATIONS

- Turns Ratio: (P1 ~ P3) : (J1 ~ J2) 1:1 ±3%
(P4 ~ P6) : (J3 ~ J6) 1:1 ±3%
- Inductance: (P1 ~ P3) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
(P4 ~ P6) 350uH min. @ 0.1V, 100KHZ, 8mA DC Bias
- Leakage Inductance: P1 ~ P3 (with J1 and J2 Short): 0.35uH Max. @ 1 MHz
P4 ~ P6 (with J3 and J6 Short): 0.35uH Max. @ 1 MHz
- Interwinding Capacitance: (P1 ~ P3) : (J1 ~ J2): 35pF max. @ 1MHZ
(P4 ~ P6) : (J3 ~ J6): 35pF max @ 1MHZ
- DC Resistance: (J1 ~ J2) : (J3 ~ J6): 1.2 Ohms Max.
- Return Loss: 1MHZ to 30MHZ: -18dB MIN
30MHZ to 60MHZ: -(18-20log(f/30))dB MIN (WHERE f IS IN MHZ)
60MHZ to 80MHZ: -12dB MIN
80MHZ to 100MHZ: (P1~P3) : (J1~J2):2250VDC
(P4~P6) : (J3~J6):2250VDC
- Dielectric Withstanding: (P1~P3) : (J1~J2):2250VDC
(P4~P6) : (J3~J6):2250VDC
- Insertion Loss: 100KHZ TO 100MHZ : -1.2dB MAX.
- Cross Talk: 1MHZ to 100MHZ: -35dB TYP
- Common to Common mode Attenuation 30MHZ to 100MHZ: -30dB TYP

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ADAM TECH

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MODULAR JACK GANGED, 1 PORT, 8P8C
W/ LED1 GREEN, LED2 YELLOW

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE INCHES [MM]		TOLERANCES: EXCEPT AS NOTED	
INCHES	HOLES		
.X ±.10	Ø.X	±.10	
.XX ±.020	Ø.XXX	±.015	
.XXX ±.015	Ø.XXX	±.010	
APPROVALS		DATE	
DRAWN	DY	12/09/10	
CHECKED			
APPROVED			
REF: S0005C	SCALE: NTS	SHEET 2 OF 2	

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